

ABSTRACT

An amplifier circuit comprises a main stage amplifier
5 connected between a RF input and a RF output and at least one
secondary stage amplifier, which is connected in parallel to
the main stage amplifier between the RF input and the RF
output. The secondary stage amplifier comprises an input
10 bipolar transistor, whose collector terminal or emitter
terminal is high frequency-coupled to the RF input. The
secondary stage amplifier further comprises an output bipolar
transistor, whose base terminal is high frequency-coupled to
the base terminal of the input bipolar transistor and whose
15 collector terminal or emitter terminal is high frequency-
coupled to the RF output. The output bipolar transistor is
further coupled to a supply voltage terminal.

Figure 4